Questions were  
  
projects, general digital logic verilog  
  
basic concepts of digital communications  
  
difference between test bench and design files etc..,  
  
  
HR was friendly  
  
Tell me something about your project  
some tricky reasoning questions  
problem faced in project time  
about your weakness etc

Which of the following is true?

A. Intel 8080 was the ancestor of Intel 8085  
B. Intel 8080 was the ancestor of Intel Z 80  
C. Intel 8080 was the ancestor of both Intel 8085 and Z 80  
D. Intel 8085 was the ancestor of Intel 8085

Answer :  C

When was the first 8-bit microprocessor introduced?

A. 1969      
B. 1974      
C. 1979      
D. 1985

Answer :  B

Which is a machine-oriented high-level language for the GEC 4080 series machines.

A. LOGO  
B. SNOBOL  
C. Babbage  
D. ALGOL

Answer :  C

A program that is employed in the development, repair or enhancement of other programs is known as

A. System software  
B. Software tool  
C. Applications program  
D. Utility program

Answer : B

A program component that allows structuring of a program in an unusual way is known as

A.  Correlation  
B.  Coroutine  
C.  Diagonalization  
D.  Quene  
Answer : B

In C , they asked me memory layout of C programs, code fibonacci, Storage Classes.In OS, I was asked Paging, Segmentation.

They ask mainly based on your field of interest written in your CV. So be sure about it.  
  
In 1st round, they asked to me to   
  
1) design asynchronous mod-10 counter.  
2) working of transistor based NOR gate and inverter for any input combination.  
3)Write an assembly program of accumulative addition of 10 numbers stored in stack i.e. from 0x00 to 0x10 ,add them to a contant number.then store it in accumulator reg.  
4)What is linker list in C ?  
  
In 2nd round, one inteviewer  asked me   
about Cache.  
Define Cache.  
Why Cache in faster ?  
Where it is located in chip?   
Give difference bet. SRAM and Cache.  
  
1) Next he ask me about MOS,How to reduce bn/bp ratio practically? what is effect on DC characteristic ?  
  
2) Explain Product life cycle(PLC).  
  
3) Explain STA(Static Timing Analysis) with example.  
  
4) Design 128:1 mux from 4:1 mux,how many MUX and selection lines are required ?  
  
5) Tell me Why should i hire u ?Atlast, he told me to ask some question if I want.  
  
In 3rd round, HR person asked me about two waveforms, so i had to identify them. Next he two me about two situation & ask me how do you tackle them ?  
  
In 4th round, two guys took my interview. It was the final panel, that I had to face. It was longest and difficult among all other rounds.  
They quickly threw questions on me one by one. There are lots of questions, but I can recall few of them.  
  
1) 1st is identify one mux based circuit, identify layout, what is total W/L ratio of layout in parallel.  
  
2) Explain set up & hold time violation in sequential & combinational ckt. Also same in single & double edge triggered flip-flop .  
  
3) One non-technical problem to check your honesty.  
  
4) Tell me something about your project that u had done in B.E & M.tech.Which problems did u face during M.Tech projects,how do u overcome it. What are limitations of your projects ? Which tools did you use ?  
  
5) What is your weakness. how do u overcome. Will you want to learn any scripting language in future ? Next, they show me one simulation command file to identify it.  
  
6) Give different suggestions to speed up invertor. --Wish u best luck ...u may be inside intel (intel inside).  
  
Be confident about resume

CMOS inverter  
VHDL programming  
Flip flops

STA concepts and problems, Implementation of all logic gates using muxes, clock gating circuits, inputs and outputs at every stage of ASIC and Physical Design flows

 HR round  
2. Tell me about yourself  
3. What are your long term goals?  
4. Are you be able to work in cross geo team?  
5. Why you wanted to pursue M.tech when you were already working in IT company. (I worked for 1 year after graduation degree)  
6. What difference you felt between actual job and internship?  
7. How you felt going back to college after 1 year experience? Does your experience helped you in any important decision?  
8. What were the things which excited you in Intel internship?  
9. How you manage your work and collaborate within team?